

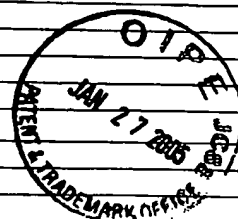
**Complete if Known**

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First Named Inventor	Gi-ho Park
Group Art Unit	2171
Confirmation No.	3389
Attorney Docket Number	5649-1159

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Sheet	1	of	1
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Attorney Docket Number	5649-1159
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FOREIGN PATENT DOCUMENTS							
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nm	5.	United Kingdom Search Report Under Section 17, United Kingdom Application No. GB0415751.7, November 11, 2004.	
m	6.	Puttaswamy et al., "System Level Power-Performance Trade-Offs in Embedded Systems Using Voltage and Frequency Scaling of Off-Chip Buses and Memory, " <i>15<sup>th</sup> International Symposium on System Synthesis</i> . pp. 225-230 (October 2-4, 2002)	

*[Handwritten signature]*

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